16bit written with CAMAC.
NIM signal outputs.

Output signals are level, output and pulse output.

3 methods of the outside synchronous output is possible.
And, in every 8bit, output signal switching.

Because the pulse of NIM and a level are output by CAMAC, module.
It is effective when a test and synchronism are taken.

≪CAMAC Function≫

F(9)・A(0)・S2 : Resetting of the output
F(16)・A(0)・S1  : 16bit Data writing
In case of level output NIM signal outputs
F(16)・A(0)・S2  : In case of internal pulse output
100nSec～1μSec  NIM signal outputs
(Z+C)・S2        : NIM signal outputs

≪ Specifications ≫

EXT. INPUT  : NIM LEVEL Minimum width 50nSec
OUTPUT      : NIM level mode, NIM pulse mode
              (100nSec～1μSec)
Form        : CAMAC Standard 1 width module
Power supply: +6V 450mA
             -6V 650mA