CAMAC

INPUT REGISTER

(C005)

◎ One unit 3 post

1. Signal is seen directly.
   INPUT REGISTER

2. Signal is seen with Strobe by GATE.
   Strobe coincidence

3. Control of the interruption is given.
   Interruption register

◎ Switching is possible by 8 input by the input.

◎ Completely, NIM signal

It is the module which tells a CAMAC controller the pulse of NIM and a level. Moreover, while it is output, it faces in the outside, and a NIM signal is taken out, and interruption（LAM）can be used for the VETO signal output of other modules.

≪CAMAC Function≫

F（0）・A（0）・S１	: Input signal, directly, reading
F（0）・A（1）・S１	: The front switch reads the signal of coincidence with the outside GATE at the time of GATE. The data that an input signal became L at the time of LEVEL become on, and the front switch reads this pattern by the occurrence of the interruption.
F（1）・A（0）・S１	: LAM MASK Reading of the register
F（2）・A（0）・S１	: F(0)・A(0)・S1 same.
F（2）・A（1）・S１	: F(0)・A(1)・S1 same.
F（2）・A（1）・S２	: Register clearance for the input
F（8）・（A（0）＋A（1））・S1	: TEST LAM
F（9）・A（1）・S２	: Register clearance for the input
F（１１）・A（0）・S２	: LAM MASK Register clearance （LAM by the input is done with MASK, and it stops going.）
F（１７）・A（0）・S１	: LAM MASK Register writing bit on Input becomes effective as to the LAM interruption.
F（２４）・A（0）・S１	: LAM Interruption Disabl
F（２４）・A（1）・S１	: LAM Outside output Disabl
F（２６）・A（0）・S１	: LAM Interruption Inabl
F（２６）・A（1）・S１	: LAM Outside output Inabl

＜ Specifications ＞

Signal, GATE, CLEAR, INPUT : NIM level Minimum width 20nSec

OUTPUT : NIM Level

Form : CAMAC Standard 1 width module

Power supply : +6V 650mA
-6V 350mA